

DIGITAL SEMICONDUCTOR BASED PRINTING SYSTEM AND METHOD

ABSTRACT

The print engine is composed of a semiconductor memory layer overlaid on an insulated conductive layer with a one to one correspondence of each memory cell with the conductive pad on the insulated layer. The entire structure can be fashioned into a either a planar structure or a cylindrical structure with insulated conductive pads providing protection to the sensitive semiconductor memory from impact loading that occurs during the printing process.

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